AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph beginning at line 9 on page 1, as follows:

The following seven Applications, including the present Application, are related: September 29, 2003, to Gustavson et al., entitled "METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING COMPOSITE BLOCKING BASED ON L1 CACHE SIZE", having IBM Docket YOR920030010US1; 2. U.S. Patent Application No. 10/______, 10/671,933, filed on _______, to on September 29, 2003, to Gustavson et al., entitled "METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING A HYBRID FULL PACKED STORAGE FORMAT", having IBM Docket YOR920030168US1; 3. U.S. Patent Application No. 10/ , 10/671,888, filed on to on September 29, 2003, to Gustavson et al., entitled "METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING REGISTER BLOCK DATA FORMAT", having IBM Docket YOR920030169US1; September 29, 2003, to Gustavson et al., entitled "METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING LEVEL 3 PREFETCHING FOR KERNEL ROUTINES", having IBM Docket YOR920030170US1; Serial No. 10/671,937 Docket No. YOR920030171US1 (YOR.465)

- 5. U.S. Patent Application No. 40/_______, 10/671,937, filed on________, to on

 September 29, 2003, to Gustavson et al., entitled "METHOD AND STRUCTURE FOR

 PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING

 PRELOADING OF FLOATING POINT REGISTERS", having IBM Docket

 YOR920030171US1;
- 7. U.S. Patent Application No. 10/ _______, 10/671,934, filed on _________, to on September 29, 2003, to Gustavson et al., entitled "METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING STREAMING", having IBM Docket YOR920030331US1, all assigned to the present assignee, and all incorporated herein by reference.